



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
(Case No. 01-1205-A)

PATENT

In re Application of:

Elad Alon, et al.

Serial No.: 10/658,710

Filed: September 8, 2003

For: Apparatus and Method for a Digital
DelayLocked Loop

)
)
) Group Art Unit: Not Assigned
)
) Examiner: Not Known
)
)
)
)
)


TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In regard to the above-identified patent application:

1. We are transmitting herewith the attached:
 1. Information Disclosure Statement
 2. Form PTO-1449
 4. Return Postcard
2. With respect to additional fees:
 - A. ☒ No additional fee is required.
 - B. ☐ Attached is a check in the amount of \$ _____
 - C. ☐ Charge the total additional fee to Deposit Account No. _____
3. Please charge any additional fees or credit overpayments to the Deposit Account No. 13-2490. A duplicate copy of this sheet is enclosed.
4. CERTIFICATE OF MAILING UNDER 37 CFR 1.8 (FIRST CLASS MAIL): The undersigned hereby certifies that this Transmittal Letter and the document(s) as described in paragraph 1 hereinabove, are being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 10 day of December, 2003.

By:


Paul W. Churilla
Reg. No. 47,495



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
(Case No. 01-1205-A)

PATENT

In re Application of:)
)
Elad Alon, et al.) Group Art Unit: Not Yet Assigned
)
Serial No.: 10/658,710) Examiner: Not Known
)
Filed: September 8, 2003)
)
For: Apparatus and Method for a Digital Delay)
Locked Loop)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to the duty of disclosure provided by 35 C.F.R. § 1.56 and §§ 1.97-98, the applicants wish to make the following references and information of record in the above-identified application. The references are also listed in the PTO-1449 form enclosed herewith. It is requested that the references and information be given careful consideration and that it be cited of record in the prosecution history of the present application so that they will appear on the face of the patent issuing from the present application. Pursuant to 37 C.F.R. § 1.98(d), copies of the references are not enclosed. These references were made of record in the parent application serial no. 10/112,963 filed March 29, 2002.

McDonnell Boehnen Hulbert & Berghoff
300 South Wacker Drive
Chicago, Illinois 60606
Telephone: (312) 913-0001
Facsimile: (312) 913-0002

In the judgment of the undersigned, portions of the references may be material to the examination of the pending claims. No representation is intended as to the relative importance of any portion of the references. This Statement is not a representation that the cited references have effective dates early enough to be "prior art" within the meaning of 35 U.S.C. §§ 102 or 103.

CITED REFERENCES

U. S. PATENTS

<u>PATENT NO.</u>	<u>DATE ISSUED</u>	<u>INVENTOR</u>	<u>CLASSIFICATION</u>	
5,614,855	03-1997	Lee et al	327	158
5,945,862	08-1999	Donnelly et al.	327	278
5,939,912	08-1999	Rehm	327	158
6,125,157	09-2000	Donnelly et al.	375	371
6,133,773	10-2000	Garlepp et al.	327	247
6,137,328	10-2000	Sung	327	158
6,147,531	11-2000	McCall et al.	327	158
6,181,178	01-2001	Choi	327	175
6,198,326	03-2001	Choi et al.	327	263
6,204,709	03-2001	Searle et al.	327	231
6,205,191	03-2001	Portmann et al.	375	354
6,229,358	05-2001	Boerstler et al.	327	116
6,229,359	05-2001	Chesavage	327	116

6,232,812	05-2001	Lee	327	277
6,247,138	06-2001	Tamura et al.	713	600
6,262,608	07-2001	O'Hearcain et al.	327	142
6,342,796	01-2002	Jung	327	141
6,396,250	05-2002	Bridge	323	283
6,480,047	11-2002	Abdel-Maguid et al.	327	161
6,486,716	11-2002	Minami et al.	327	152

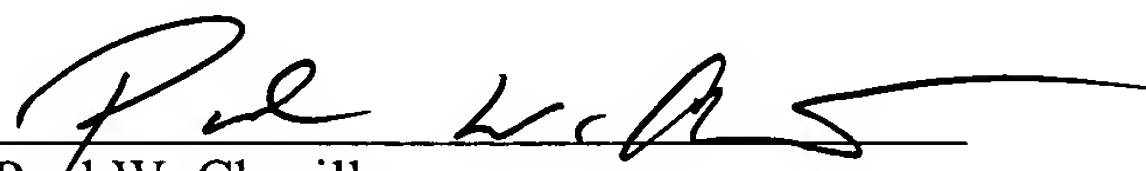
Other Documents

Jung-Bae Lee, et al., *Digitally-Controlled DLL, and I/O Circuit for 500Mb/s/pin x16 DDR SDRAM*, ISSCC 2001, Session 4 High-Speed Digital Interfaces, 4.6, February 5, 2001, 2 pages.

Respectfully submitted,


McDonnell Boehnen Hulbert & Berghoff

Date: Dec. 10, 2003

By: 
 Paul W. Churilla
 Reg. No. 47,495

CERTIFICATE OF MAILING

The undersigned hereby certifies that the foregoing INFORMATION DISCLOSURE STATEMENT is being deposited as first class mail, postage prepaid, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 10 day of December, 2003.



Paul W. Churilla

FORM PTO-1449
(Rev. 2-32)

U.S. Department of Commerce
Patent and Trademark Office

Atty. Docket No.

Serial No.

01-1205-A

10/658,710

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(Use several sheets if necessary)

Applicant: Elad Alon, et al.

Filing Date:
09/08/2003

Group:

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
	5,614,855	03-1997	Lee et al	327	158	8/21/95
	5,945,862	08-1999	Donnelly et al.	327	278	7/31/97
	5,939,912	08-1999	Rehm	327	158	6/18/97
	6,125,157	09-2000	Donnelly et al.	375	371	2/6/97
	6,133,773	10-2000	Garlepp et al.	327	247	10/10/97
	6,137,328	10-2000	Sung	327	158	5/28/99
	6,147,531	11-2000	McCall et al.	327	158	7/9/98
	6,181,178	01-2001	Choi	327	175	7/21/99
	6,198,326	03-2001	Choi et al.	327	263	11/13/98
	6,204,709	03-2001	Searle et al.	327	231	9/30/99
	6,205,191	03-2001	Portmann et al.	375	354	7/21/97
	6,229,358	05-2001	Boerstler et al.	327	116	12/15/99
	6,229,359	05-2001	Chesavage	327	116	12/31/99
	6,232,812	05-2001	Lee	327	277	11/20/98
	6,247,138	06-2001	Tamura et al.	713	600	6/8/98
	6,262,608	07-2001	O'Hearcain et al.	327	142	5/15/00
	6,342,796	01-2002	Jung	327	141	12/19/00
	6,396,250	05-2002	Bridge	323	283	8/31/00
	6,480,047	11-2002	Abdel-Maguid et al.	327	161	5/17/01
	6,486,716	11-2002	Minami et al.	327	152	9/5/00



FORM PTO-1449
(Rev. 2-32)

U.S. Department of Commerce
Patent and Trademark Office

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use several sheets if necessary)

Atty. Docket No.

01-1205-A

S rial No.

10/658,710

Applicant: Elad Alon, et al.

Filing Date:


09/08/2003

Group:

FOREIGN PATENT DOCUMENTS

		Document Number							Date	Country	Class	Subclass	Translation	
													Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc).

			Jung-Bae Lee, et al., <i>Digitally-Controlled DLL, and I/O Circuit for 500Mb/s/pin x16 DDR SDRAM</i> , ISSCC 2001, Session 4 High-Speed Digital Interfaces, 4.6, February 5, 2001, 2 pages
EXAMINER			
DATE CONSIDERED			

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.